



04-24-02

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PTO/SB/21 (08-00)

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/961,114
	Filing Date	Sep 20, 2001
	First Named Inventor	Perino et al.
	Group Art Unit	2833
	Examiner Name	Figueroa, Felix O.
Total Number of Pages in This Submission	Attorney Docket Number	RB1-035USC3

ENCLOSURES (check all that apply)		
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<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):
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Firm or Individual name	Daniel L. Hayes
Signature	<i>Daniel L. Hayes</i>
Date	4/22/02

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.09/961,114
Filing Date Sep 20, 2001
Inventorship Perino et al.
Applicant Rambus Inc.
Group Art Unit 2833
Examiner Figueroa, Felix O.
Attorney's Docket No. RB1-035USC3
Title: Chip Socket Assembly and Chip File Assembly for Semiconductor Chips

#6
9-25-02
JuriRESPONSE TO 04/03/02 OFFICE ACTION

To: Commissioner of Patents and Trademarks
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TECHNOLOGY CENTER 2800REMARKS

In response to the election requirement set forth in the 04/03/02 Office Action, Applicant hereby elects claims 68-88 to be the subject of further examination in this application.

Respectfully Submitted,

Dated: 4/22/02By: Daniel L. Hayes
Daniel L. Hayes
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